**Part A**

**Objective**

Understand Encoders Decoders and Block Circuit Diagrams

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| 1. Complete block diagram below |
| |  |  | | --- | --- | | A |  | |

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| 1. Using logic.ly complete logic gate level circuit for the block circuit diagram below |
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**Hand up this practical report at the end of session and ensure it has been checked**

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| **Student Name** |  | **Student Number** |  |
| **Date** |  | **Checked** |  |
| **Group** | **A / B** |  |  |